

starting execution of the program commands, via the processor, stored in the other memory area;

controlling the transfer of program commands, via the processor, in the reload memory unit to the original area; and

5 switching the processor to a defined initial state, after the transfer operation, by switching over the reset signal.

11. A process for starting a data processing installation as claimed in claim 1, the process further comprising the steps of:

10 compressing the program command stored in the reload memory unit using a compression process;

storing a decompression process in one of a portion of the program commands in the bootstrap memory unit and an uncompressed portion of the program commands in the reload memory unit; and

15 compressing the compressed program commands when the program commands for the decompression process are executed.

12. A process for starting a data processing installation as claimed in claim 10, the process further comprising the steps of:

20 storing the program commands for the decompression process in the bootstrap memory unit;

setting the processor, after the bootstrap transfer operation, to the defined initial state;

25 changing the address of at least one portion of the bootstrap program before the reload transfer operation by copying the at least one portion;

storing program commands in the original address range as part of the reload transfer operation; and

setting the processor, after the reload transfer operation, to the defined initial state.

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13. A process for starting a data processing installation as claimed in claim 10, the process further comprising the steps of:

storing the program commands for the compression process in the reload memory unit;

5 setting the processor, after the bootstrap transfer operation, to the defined initial state;

changing the address of at least one portion of the bootstrap program before the reload transfer operation by copying the at least one portion;

10 storing program commands for the decompression process in the original address range in a first phase of the reload transfer operation;

setting the processor, after the first phase of the reload transfer operation, to the defined initial state;

changing the address of at least one portion of the program commands for the decompression process by copying the at least one portion;

15 storing program commands of an operating system, in a second phase of the reload operation, in the original address range; and

setting the processor, after the second phase of the reload operation, to the defined initial state again.

20 14. A data processing installation, comprising:

a processor for executing program commands;

a bootstrap memory unit for storing a bootstrap program;

a reload memory unit for storing program commands; and

25 a main memory unit to which program commands from the reload memory unit are transferred using the bootstrap program before execution by the processor; ,

wherein at least one of the bootstrap memory unit and the reload memory unit is one of a memory unit with serial data access and a memory unit which requires a plurality of read access operations in order to read a program command for the processor.

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15. A data processing installation as claimed in claim 14, further comprising:

5 a control unit which operates without a program and, when the data processing installation is turned on, transfers the bootstrap program from the bootstrap memory unit to the memory unit, the bootstrap memory unit outputting the bit positions of the program commands of the bootstrap program serially or using a plurality of read operations per program command.

16. A circuit arrangement, comprising:

10 an interface to a processor;

an interface to one of a bootstrap memory unit with serial data access and a bootstrap memory unit which requires a plurality of read access operations in order to read a program command for the processor;

15 an interface to one of a reload memory unit with serial data access and a reload memory unit which requires a plurality of read access operations in order to read a program command for the processor;

an interface to a main memory unit with parallel data access for reading a program command; and

20 a control unit which in response to a start signal, prompts a bootstrap transfer operation for transferring program commands for the processor from the bootstrap memory unit to the main memory unit, and which, after the bootstrap transfer operation, prompts the processor to execute the program commands transferred to the main memory unit, and which permits a reload transfer operation in which program commands are transferred from the reload memory unit to the  
25 main memory unit.

17. A memory unit, formed as one of a serial-access memory unit and a memory unit which requires a plurality of read access operations in order to read a program command stored in it, the memory unit used for program commands in a  
30 start operation for a data processing installation.